

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

#612/I.DS  
R.T. 11/3/02

Art Unit: Not Yet Assigned )  
Examiner: Not Yet Assigned )  
Inventor: Koichi Miyasaka )  
Serial No. Not Yet Assigned )  
Filed: Herewith )  
For: **CONDUCTOR-PATTERN TESTING** )  
**METHOD, AND ELECTRO-OPTICAL** )  
**DEVICE** )

INFORMATION DISCLOSURE  
STATEMENT



Hon. Commissioner of Patents & Trademarks  
Washington, D. C. 20231

CERTIFICATE OF EXPRESS MAILING

*I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on*

By

*Sept 17, 2000*  
*[Signature]*

Sir:

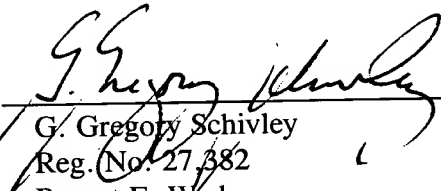
In accordance with 37 C.F.R. 1.56, the Examiner is hereby respectfully advised that the document listed on the attached form PTO-1449 is known by or has come to the attention of Applicant, and is being submitted herewith for consideration by the Examiner. Further, a copy of the listed reference is also included.

The reference listed on the form PTO-1449 is being cited only in the interest of candor and without any admission that it constitutes statutory prior art or contains matter which anticipates the invention or which would render the same obvious to a person of ordinary skill in the art.

Respectfully submitted,

Dated: September 19, 2000

By: \_\_\_\_\_

  
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